



LIST OF PUBLICATIONS CITED BY APPLICANT			Atty. Docket No. SEL 205 DIV	Serial No. 10/034,797		
			Applicant Yukio TANAKA	<b>RECEIVED</b>  FEB 23 2004		
			Filing Date December 28, 2001			
U.S. PATENT DOCUMENTS						
*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	4,398,207 ✓	08/09/83	Hoff, Jr. et al	357	51	05/12/81
	4,801,819 ✓	01/31/89	Theus et al	307	269	06/29/87
	5,170,158 ✓	12/08/92	Shinya	340	800	06/28/90
	5,570,105 ✓	10/29/96	Koyama	345	98	12/21/94
	5,572,211 ✓	11/05/96	Erhart et al	341	144	01/18/94
	5,589,847 ✓	12/31/96	Lewis	345	98	09/23/91
	5,648,277 ✓	07/15/97	Zhang et al	437	21	11/02/94
	5,731,774 ✓	03/24/98	Fujii et al	341	144	08/28/96
	5,793,348 ✓	08/11/98	Lee et al	345	98	11/09/95
	5,801,673 ✓	09/01/98	Shimada et al	345	100	08/29/94
	5,977,940 ✓	11/02/99	Akiyama et al	345	94	03/06/97
	6,040,812 ✓	03/21/00	Lewis	345	89	06/19/96
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	6,111,557 ✓	08/29/00	Koyama et al	345	89	12/29/97
	6,281,891 B1 ✓	08/28/01	DaCosta et al	345	206	06/02/95
	6,459,395 B1 ✓	10/01/02	Kida et al	341	126	10/27/00
	6,600,436 B2 ✓	07/29/03	Tanaka	341	150	03/25/02
	6,614,376 B2 ✓	09/02/03	Tanaka et al	341	144	11/21/01
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	DOCUMENT NUMBER	DATE	APPLICANT	English Abstract	English Trans.	FILING DATE
	JP 58-030231	02/22/83	Seiko Epson Corp.	X		08/18/81
	JP 06-208343	07/26/94	Toshiba KK	X		01/12/93
	JP 07-135318	05/23/95	Semiconductor Energy Lab Co. Ltd	X		11/05/93
	JP 08-078329	03/22/96	Semiconductor Energy Lab Co. Ltd	X		09/05/94
	JP 08-237097	09/13/96	Matsushita Denki Sangyo KK	X		02/24/95
	JP 09-009275	01/10/97	Sony Corp.	X		06/16/95
	JP 11-142807	05/28/99	NEC IC Micro- computer Systems Ltd.	X		11/13/97



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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

(Include name of author (in CAPITAL LETTERS), title of article or item (book, magazine, journal, serial, symposium, catalog, etc.) date, pages(s), volume-issue number(s), publisher, city and/or country where published).

- 1) LEWIS, A.G. et al, "Switched-Capacitor Circuits Using Polysilicon on Quartz Thin-Film Technology," ISSCC 92/Session 7/Circuits for Transducers and Active-Matrix LCD Drivers/Paper 7.3, IEEE International Solid State Circuits Conference, pp. 122-123 (1992).
- 2) FURUHASHI, T. et al, "A 64-Gray-Scale Digital Signal Driver for Color TFT-LCDs," SID 94 Digest, pp. 359-362 (1994).
- 3) MORITA, T. et al, "VGA Driving with Low Temperature Processed Poly Si TFTs," IDEM 95, pp. 841-843 (1995).
- 4) YAMAMOTO, Y. et al, "High Performance Low Temperature Poly-Si TFT with Self-Aligned Offset Gate Structure by Anodic Oxidation of Al for a Driver Monolithic LCDs," Asia Display '95, pp. 941-942 (1995).
- 5) MATSUEDA, S. et al, "Low-Temperature Poly-Si TFT-LCD with Integrated 6-bit Digital Data Drivers," SID 96 Digest, pp. 21-24 (1996).
- 6) KOYAMA, J. et al, "Low-Temperature Poly-Si TFT-LCDs with Digital Interface," Extended Abstracts of the 1997 International Conference on Solid State Devices and Materials, Hamamatsu, pp. 348-349 (1997).
- 7) OHTANI, H. et al, "Late-News Poster: A 60-in. HDTV Rear-Projector with Continuous-Grain-Silicon Technology," SID 98 Digest, pp. 467-470 (1998).
- 8) OSAME, M. et al, "Poly-Si TFT-LCD HDTV Display with Monolithic Integrated 8-bit Digital Data Drivers," SID 98 Digest, pp. 1059-1062 (1998).

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\*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP form. Draw line through citation if not in conformance and not considered. Include a copy of this form with the next communication to applicant.

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LIST OF PUBLICATIONS CITED BY APPLICANT		Attorney Docket No. SEL 205 DIV		Serial No. Not Assigned	
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		Filing Date Herewith		Group	

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*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
M I	5,434,599	07/1995	Hirai et al	345	100	
	5,643,826	07/01/97	Ohtani et al	437	88	10/25/94
	5,703,617	12/1997	Callahan, Jr. et al	345	98	
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	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
M I	JP 7-130652	05/19/95	Semiconductor Energy Laboratory Co., Ltd.	/		10/29/93
	EP 920 135 A2	06/02/99	Semiconductor Energy Laboratory Co., Ltd.			11/25/98
	JP 11-167373	06/22/99	Semiconductor Energy Laboratory Co., Ltd.			05/11/98
	EP 938 074 A1	08/25/99	Semiconductor Energy Laboratory Co., Ltd.			10/01/98
	JP 20-00036749	02/02/00	Semiconductor Energy Laboratory Co., Ltd.			11/25/98
OTHER PUBLICATIONS (Including Author, Title, Date, Pertinent Pages)						
M I	1) English Abstract re Japanese Patent Application No. JP 7-130652, published May 19, 1995. 2) English Abstract re Japanese Patent Application No. JP 11-167373, published June 22, 1999. 3) English Abstract re Japanese Patent Application No. JP 20-00036749, published February 2, 2000. 4) U.S. Patent Application No. 09/162,230 (pending) entitled "Semiconductor Display Device and Method of Driving the Same." 9/29/1998 5) U.S. Patent Application No. 09/197,766 (pending) entitled "D/A Conversion Circuit and Semiconductor Device." 11/23/1998					

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